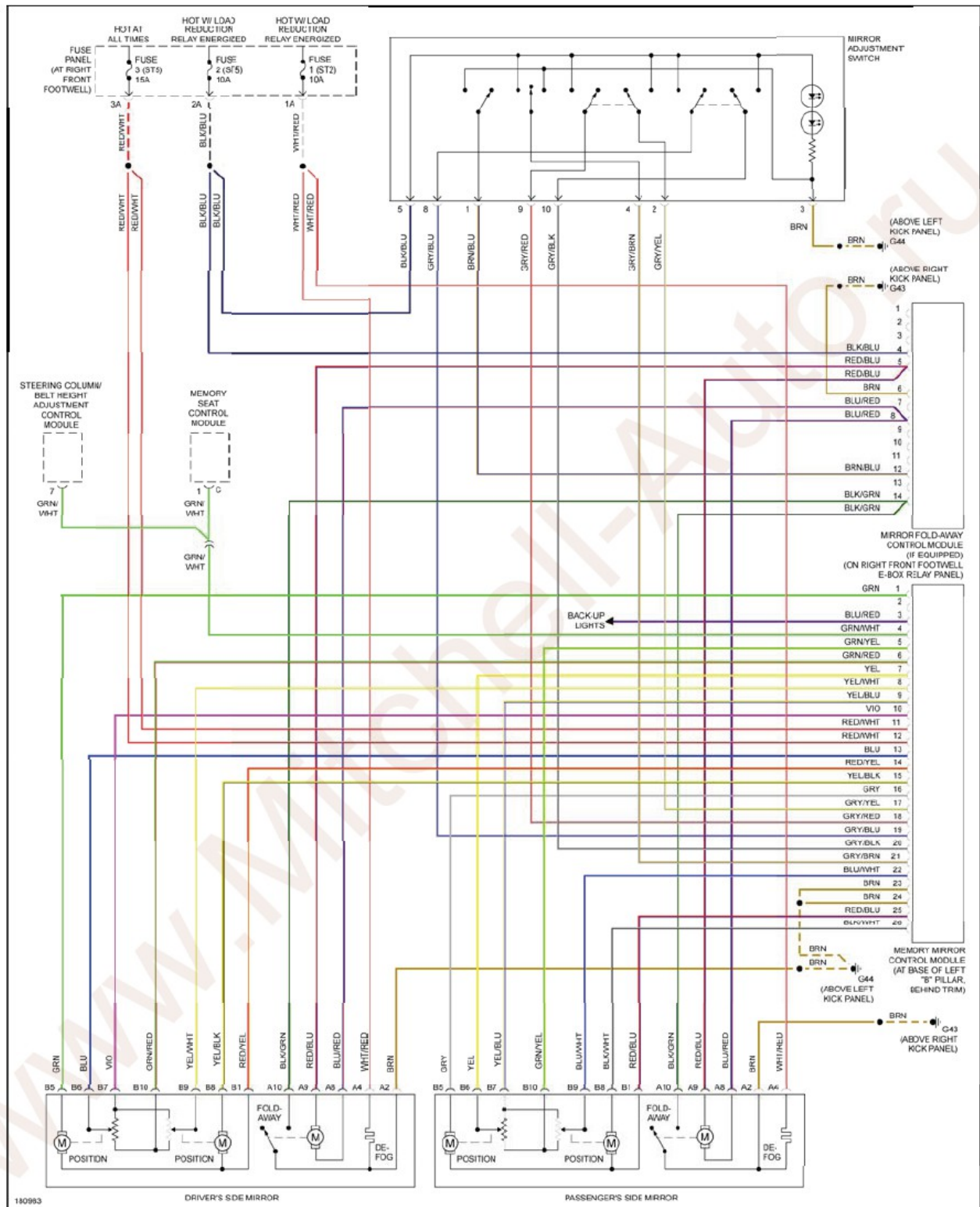




Memory Mirrors Circuit